

Dec. 2014

Silicon Labs Reference design crystal for Si534x series

(Clock generator / Jitter Attenuator)

● Application

**Network / Storage / Telecom /
Medical / Video / xDSL**



Silicon Labs Si534x series Reference Design

Silicon Labs New chip

Silicon Labs P/N	Function
Si 5340 / 41	Clock Generator
Si 5342 /44 /45	Jitter-Attenuator with built in Multi-synthesizer
Si 5346 / 47	Jitter-Attenuator without Multi-synthesizer

Qualified AVX / KYOCERA part list

KCD P/N	Initial tolerance ppm	Stability -40 to +85degC ppm	Co max pF	ESR max. ohm	CL pF	Pulling ppm/C typ	Drive level uW max	Activity Dip test
CX3225SB48000D0FPJC1	10	15	2.0	23	8.0	16.0	200	No
CX3225SB48000D0WPSC1	15	30						
CX3225SB48000D0WPTC1	30	60						
CX3225SB54000D0FPJC1	10	15				31.0		
CX3225SB54000D0WPSC1	15	30						
CX3225SB54000D0WPTC1	30	60						
CX3225SB48000D0FPJC2	10	15	2.0	23	8.0	16.0	200	Yes
CX3225SB48000D0WPSC2	15	30						
CX3225SB54000D0FPJC2	10	15				31.0		
CX3225SB54000D0WPSC2	15	30						

- **Size, Frequency are decided by requirement of end customer. It depends on their application and required accuracy.**